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PUBLICATIONS AND PRESENTATIONS

1. H.-C. Chi, H.-C. Chang, and K.-Y. Liu, ‘‘The Design and Implementation of a General-Purpose Microprogrammable Digital Signal Processor,’’ *National Computer Symposium*, Hsinchu, Taiwan (1983).
2. S.-C. Pei and H.-C. Chi, ‘‘A General-Purpose Microprogrammable Digital Signal Processor,’’ *Telecommunications Symposium*, Taiwan (1985).
3. Y. Tamir and H.-C. Chi, ‘‘Symmetric Crossbar Arbiters for VLSI Communication Switches,’’ Computer Science Department Technical Report CSD-900028, University of California, Los Angeles, CA (October 1990).

4. H.-C. Chi and Y. Tamir, ‘‘Decomposed Arbiters for Large Crossbars with Multi-Queue Input Buffers,’’ Computer Science Department Technical Report CSD-910060, University of California, Los Angeles, CA (August 1991).

5. H.-C. Chi and Y. Tamir, ‘‘Decomposed Arbiters for Large Crossbars with Multi-Queue Input Buffers,’’ *International Conference on Computer Design*, Cambridge, MA, pp. 233-238 (October 1991).

6. Y. Tamir and H.-C. Chi, ‘‘Symmetric Crossbar Arbiters for VLSI Communication Switches,’’ *IEEE Transactions on Parallel and Distributed Systems* **4**(1), pp. 13-27 (January 1993).

7. H.-C. Chi and Y. Tamir, ‘‘Starvation Prevention for Arbiters of Crossbars with Multi-Queue Input Buffers,’’ *COMPCON*, San Francisco, CA, pp. 292-297 (February 1994).